

**DFS Logic Configuration Guide**

Dec-08

G4HUP

**74161 Programmable Divider Guide**

(applies to Issue 1 and Issue 2 PCBs)

For a division ratio of  $n$ , the 74161 requires a programmed input of  $16-n$ 

Ratio, $n$	Prog Code	Div 1 (IC402)				Div 2 (IC403)			
		SJ4	SJ3	SJ2	SJ1	SJ12	SJ11	SJ10	SJ9
2	14	1	1	1	0	1	1	1	0
3	13	1	1	0	1	1	1	0	1
4	12	1	1	0	0	1	1	0	0
5	11	1	0	1	1	1	0	1	1
6	10	1	0	1	0	1	0	1	0
7	9	1	0	0	1	1	0	0	1
8	8	1	0	0	0	1	0	0	0
9	7	0	1	1	1	0	1	1	1
10	6	0	1	1	0	0	1	1	0
11	5	0	1	0	1	0	1	0	1
12	4	0	1	0	0	0	1	0	0
13	3	0	0	1	1	0	0	1	1
14	2	0	0	1	0	0	0	1	0
15	1	0	0	0	1	0	0	0	1
16	0	0	0	0	0	0	0	0	0

Key:            1    link to pull up resistor via wire bridge  
                   0    link to ground with solder blob across pads

**Divider Configuration** - settings of SJ17 and SJ18 (Iss 2 PCB only)

Count Mode	SJ17	SJ18
Cascade (synchronous)	2-3	2-3
Independent	1-2	1-2